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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/972,100	10/05/2001	Andrew Rankin	LS130US01(LS1 01-225)	4348	
75	90 12/18/2003		EXAM	EXAMINER	
LSI LOGIC CORPORATION			THOMPSON, ANNETTE M		
MAIL STOP D- 1551 McCARTI	-106 HY BOULEVARD		ART UNIT PAPER NUMBER		
MILPITAS, CA	A 95035		2825		
			DATE MAILED: 12/18/200	3	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	<u>.</u>
•	09/972,100	RANKIN, ANDREW	
Office Action Summary	Examiner	Art Unit	
	A. M. Thompson	2825	AW
The MAILING DATE of this communication		vith the correspondence add	ress
Period for Reply		40.1711/0.55014	
A SHORTENED STATUTORY PERIOD FOR THE MAILING DATE OF THIS COMMUNICATE OF THIS COMMUNICATE OF STATE OF THIS COMMUNICATE OF THIS	ATION. 37 CFR 1.136(a). In no event, however, may a cation. lays, a reply within the statutory minimum of thiory period will apply and will expire SIX (6) MO by statute, cause the application to become A	reply be timely filed irty (30) days will be considered timely. NTHS from the mailing date of this com BANDONED (35 U.S.C. § 133).	nmunication.
1) Responsive to communication(s) filed	on <u>23 Se<i>ptember 2003</i>.</u>		
2a)⊠ This action is FINAL . 2b)	☐ This action is non-final.		
3) Since this application is in condition fo closed in accordance with the practice	r allowance except for formal ma under <i>Ex part</i> e <i>Quayl</i> e, 1935 C.I	tters, prosecution as to the r D. 11, 453 O.G. 213.	merits is
Disposition of Claims			
4) ☐ Claim(s) 1-27 is/are pending in the app 4a) Of the above claim(s) is/are 5) ☐ Claim(s) 1-9 and 17-25 is/are allowed. 6) ☐ Claim(s) 10-16,26 and 27 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction	withdrawn from consideration.		
Application Papers	·		
9) The specification is objected to by the 8 10) The drawing(s) filed on is/are: a Applicant may not request that any objection Replacement drawing sheet(s) including the 11) The oath or declaration is objected to be	n) accepted or b) objected to on to the drawing(s) be held in abeya e correction is required if the drawing	ance. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFF	
Priority under 35 U.S.C. §§ 119 and 120			
12) Acknowledgment is made of a claim for a) All b) Some * c) None of: 1. Certified copies of the priority do 2. Certified copies of the priority do 3. Copies of the certified copies of application from the Internationa * See the attached detailed Office action for since a specific reference was included in 37 CFR 1.78. a) The translation of the foreign language 14) Acknowledgment is made of a claim for reference was included in the first senter	ocuments have been received. Incuments have been received in a street the priority documents have been all Bureau (PCT Rule 17.2(a)). If or a list of the certified copies not domestic priority under 35 U.S.C in the first sentence of the specificage provisional application has a domestic priority under 35 U.S.C	Application No n received in this National S t received. \$\cdot\\$\ \\$\ 119(e)\ (to a provisional a cation or in an Application D been received. \$\cdot\\$\\$\\$\ 120\ and/or 121\ since a	application) Data Sheet. specific
Attachment(s)			
1) ☑ Notice of References Cited (PTO-892) 2) ☑ Notice of Draftsperson's Patent Drawing Review (PTC 3) ☑ Information Disclosure Statement(s) (PTO-1449) Pape	9-948) 5) Notice of	Summary (PTO-413) Paper No(s) Informal Patent Application (PTO-	

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DETAILED ACTION

Applicant's <u>Amendment B</u> to 09/972,100 has been examined. The specification is amended. Claims 26 and 27 are added. Claims 1-27 are pending.

1. Applicant's Amendment is persuasive in part. The pertinent rejections/objections of the prior office action are incorporated herein.

Specification

2. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed. Applicant's claims are primarily directed to a method that includes a *Verilog to Spice* converter *and a Spice to Verilog* converter. Appropriate correction is required.

The following title is suggested: Design Methods Using Spice to Verilog and Verilog to Spice Translation.

Drawings

- 3. Figures 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.
- 4. For Figure 1, reference Lin et al., U.S. Patent 5,581,562, Figure 1B.

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

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(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Rejection of claims 10-16

- 6. Claims 10-16 are rejected under 35 U.S.C. 102(b) as being anticipated by the Michael Naum et al. (Naum) paper entitled <u>Automatic Functional Model Validation</u>

 Between Spice and Verilog. The Naum paper outlines a method that uses SPICE models in conjunction with Verilog models to validate the functionality of a Verilog library.
- 7. Pursuant to claims 10-16, the Naum paper §§ II and IV. discloses the limitations of these claims.
- 8. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Rejection of claims 26 and 27

- 9. Claims 26 and 27 are rejected under 35 U.S.C. 102(e) as being anticipated by the Zebardast et al. paper entitled <u>SP2V</u>: Accelerating Post-Layout Spice Simulation <u>Using Verilog Gate-Level Modeling</u>.
- 10. Pursuant to claims 26 and 27, Zebardast teaches converting a Verilog block design to Spice format and simulating the design.

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Allowable Subject Matter

11. Claims 1-9 and 17-25 are allowed.

12. The following is a statement of reasons for the indication of allowable subject matter: The prior art does not teach or suggest a method of circuit design wherein one function block of the design, designed in Verilog and converted to SPICE, is combined and simulated with another function block of the design already designed in SPICE.

Remarks

13. Applicant asserts lack of knowledge for labeling Figure 1 –Prior Art--. However, the figure 1 process disclosed by Applicant as a non-specific "overview of a design process", is well known in the art of integrated circuit design. See e.g., U.S. Patent 6,470,482 to Rostoker et al., in particular Figures 9 and 17 and Lin et al. U.S. Patent 5,581,562, cited supra.

Conclusion

- 14. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Please reference the PTO-892 for a complete listing.
- 15. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not

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mailed until after the end of the THREE-MONTH shortened statutory period, then the

shortened statutory period will expire on the date the advisory action is mailed, and any

extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

the advisory action. In no event, however, will the statutory period for reply expire later

than SIX MONTHS from the date of this final action.

16. Any inquiry concerning this communication or earlier communications from the

Examiner should be directed to A.M. Thompson whose telephone number is (703) 305-

7441. The Examiner can usually be reached Monday thru Friday from 8:00 a.m. to 5:00

p.m. If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's

supervisor, Matthew S. Smith, can be reached on (703) 308-1323.

Any inquiry of a general nature or relating to the status of this application or

proceeding should be directed to the receptionist whose telephone number is (703) 308-

0956 or the Customer Service Center whose telephone number is (703) 306-3329.

17. Responses to this action should be mailed to:

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

or faxed to:

(703) 872-9306, (for all **OFFICIAL** communications intended for entry)

Hand-delivered responses should be brought to Crystal Plaza 4, 2021 South Clark

Place, Arlington, VA., Fourth Floor (Receptionist).

A. M. THOMPSON

Master's Level Patent Examiner Technology Center 2800

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